

Towards the Fairness of Traffic Policer

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Content Providers



Content Providers

Users





Application Requirements



Application Requirements



Application Requirements







Enforce Traffic Policies (Throttle Traffic Rate)





















Token Bucket Algorithm



Token Num < Packet Length









Feedback: Only Packet Loss



Feedback: Only Packet Loss



Loss-sensitive

Feedback: Only Packet Loss



Feedback: Only Packet Loss



Feedback: Only Packet Loss



Traditional CC vs. new CC -------> Unfairness

The Unfairness Problem



The Unfairness Problem

Copa occupies 99.8% bandwidth

PCC Vivace occupies 93.8% bandwidth



I CUBIC vs. I Copa

I CUBIC vs. I PCC Vivace

More in Paper: How new CC occupies the majority of bandwidth

How to Tackle the Unfairness Problem?

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Improve Congestion Control



Improve Congestion Control

Not Practicable: Content Providers want higher bandwidth



Not Practicable: Content Providers want higher bandwidth


Not Practicable: Content Providers want higher bandwidth

Inflate RTT Increase Overhead



Observation Bandwidth is allocated in the unit of Tokens



























Address Challenges #1



Token Bucket

Address Challenges #1



Token Bucket

Address Challenges #1



Token Bucket

Address Challenges #I



Residual bucket space instead of # of available tokens











Challenges #2: Maintain Per-flow Data

















Address Challenges #2: Count-min Sketch



Accuracy of Count-min Sketch with lots of flows?

Accuracy of Count-Min Sketch

• The estimation error is bounded

Theorem 1. The estimation error is within ϵB with a probability of $1 - \delta$, where $\epsilon = e/w$ and $\delta = 1/e^d$.

- Employing a large Count-Min Sketch is not exorbitant
 - A counter is no larger than *B* (bucket size)
 - A counter only needs $\Theta(log_2B)$ bits
 - E.g., 100KB bucket, 4×4096 sketch
 - 2B memory for a counter (40B granularity)
 - 32KB memory for the total sketch
 - Commercial switching chip: MBs memory

FairPolicer: On Packet Arrival



FairPolicer: On Packet Arrival
































Design of FairPolicer

- More details in the paper
 - Dynamically adjust the per-flow bucket space
 - Parameter settings

Evaluation — Testbed Setup



Evaluation — Fairness



Ensure fairness regardless of CC algorithms

Evaluation — Fairness



Unfairness Index is within 0.004 with 4x4096 sketch size (32KB memory)

Evaluation — Latency



Evaluation — Latency



Evaluation — Accuracy of Count-min Sketch



Scale to 2K flows with a sketch size of 4x4096 (32KB memory)

Conclusion

• Discover and validate the unfairness problem

 Propose FairPolicer that can fairly allocate bandwidth regardless of CC algorithms

Prototype and evaluate FairPolicer in a testbed

Thank you! Q&A

Source code: <u>https://github.com/ants-xjtu/fairpolicer</u>